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(54) **OLED DISPLAY DEVICE AND
ENCAPSULATION METHOD THEREOF**

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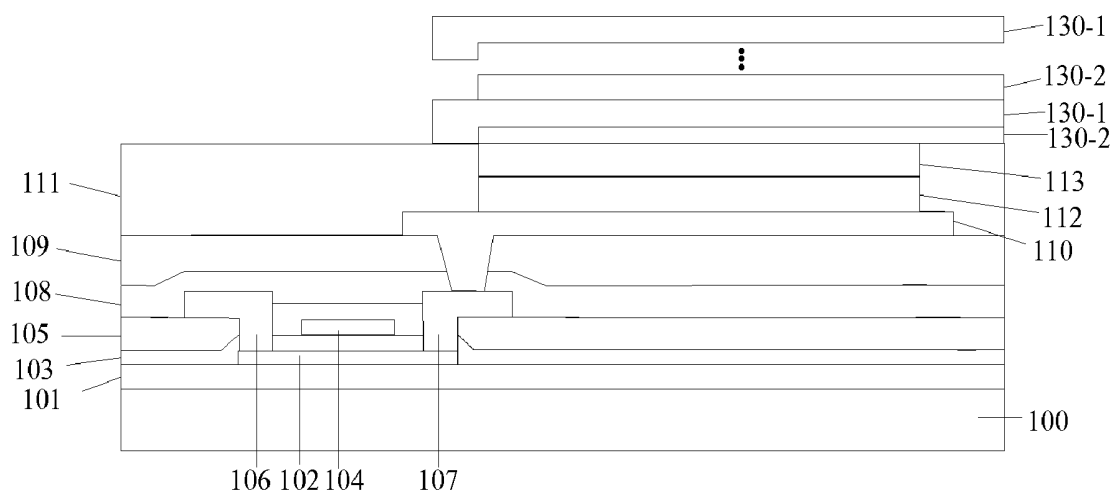
H01L 51/52 (2006.01)

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(57) **ABSTRACT**

Embodiments of the disclosure provide an Organic Light-Emitting Diode (OLED) display device and an encapsulation method thereof. The encapsulation method of the OLED display device comprises: providing a display substrate, the display substrate having a display region and a peripheral region provided outside the display region; forming a dam in the peripheral region of the display substrate; and forming a plurality of thin film encapsulation layers on the display substrate by using a single mask plate, wherein the plurality of thin film encapsulation layers envelop the dam therein.



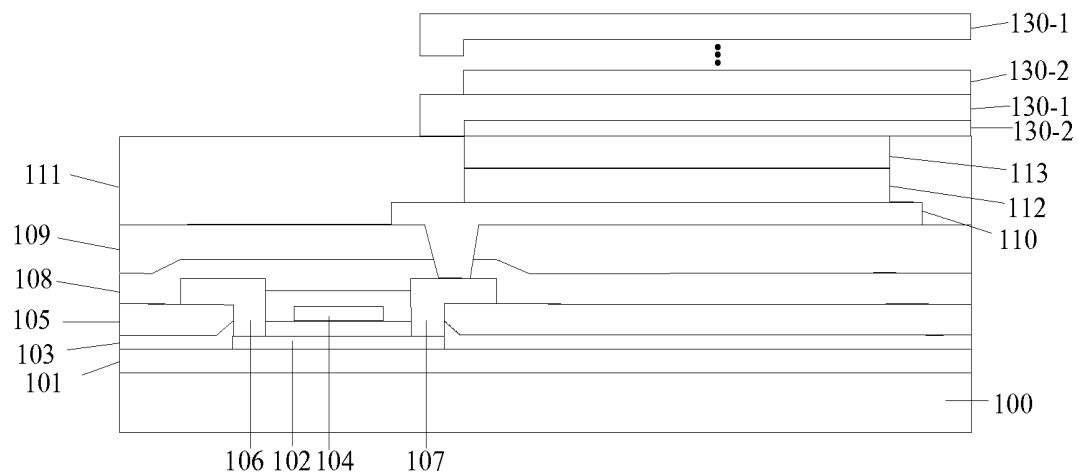


FIG. 1

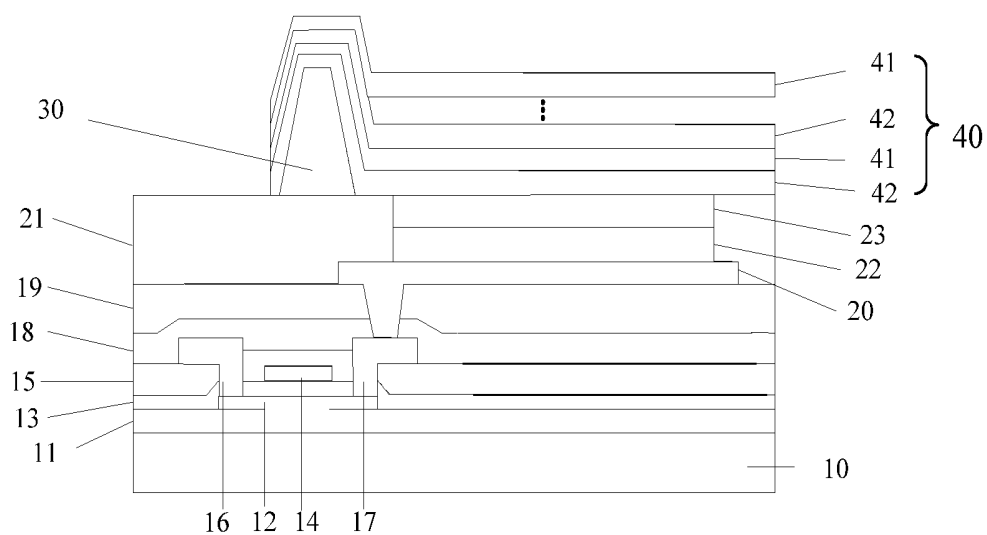


FIG. 2

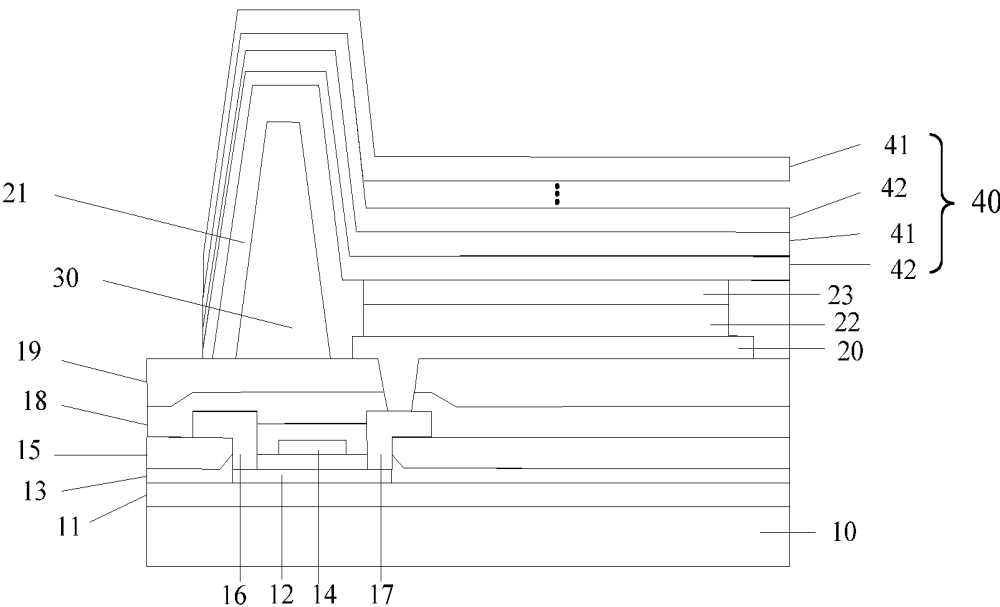


FIG. 3

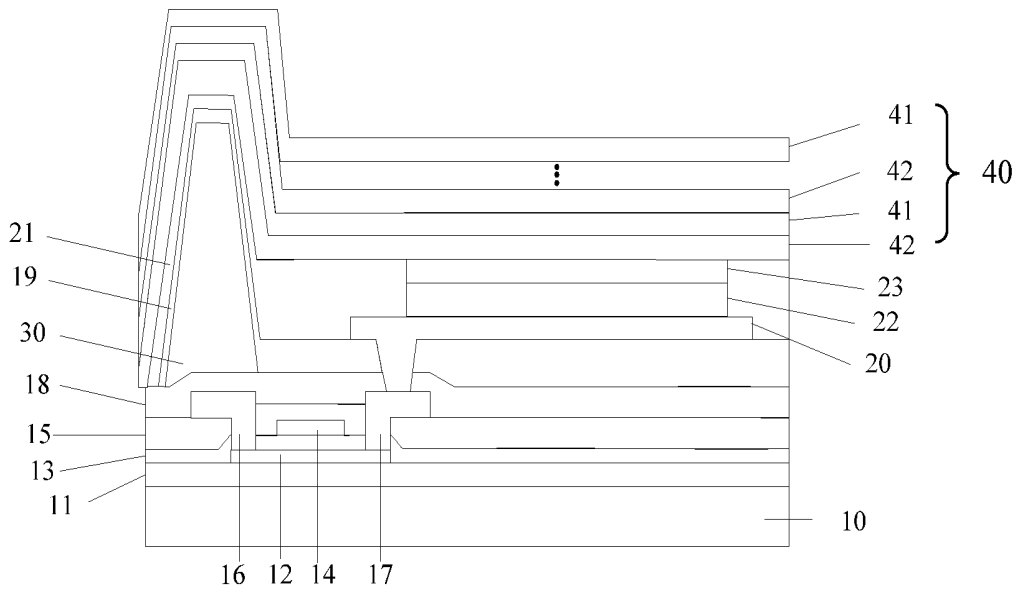


FIG. 4

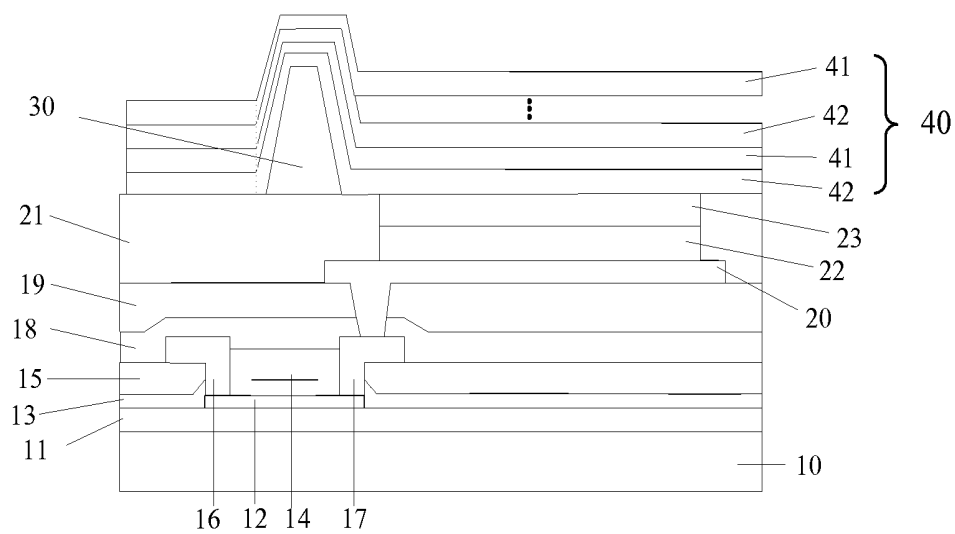


FIG. 5

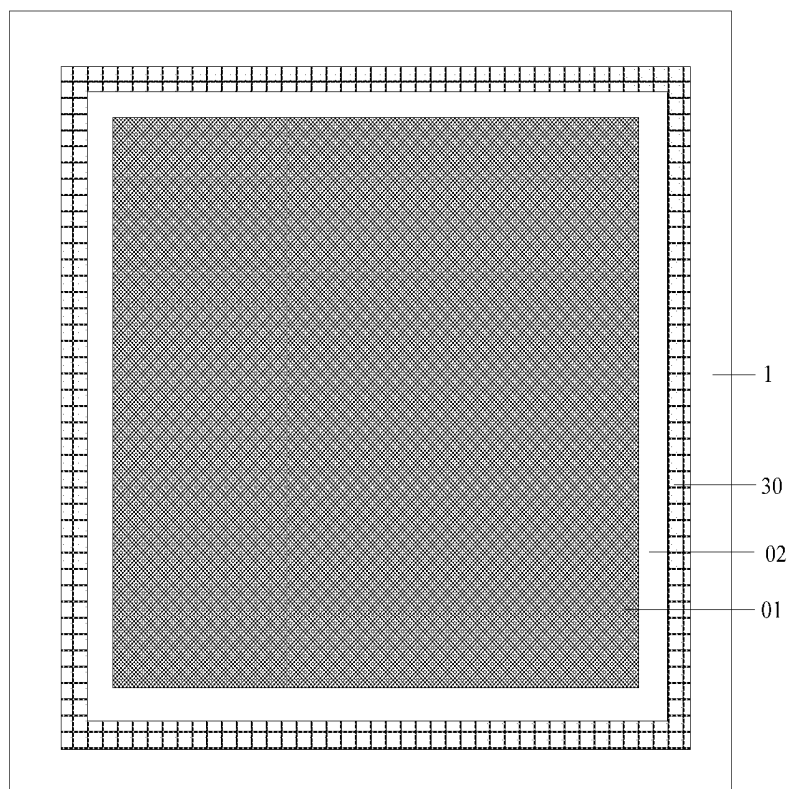


FIG. 6

OLED DISPLAY DEVICE AND ENCAPSULATION METHOD THEREOF

TECHNICAL FIELD

[0001] Embodiments of the disclosure relate to an Organic Light-Emitting Diode (OLED) display device and an encapsulation method thereof.

BACKGROUND

[0002] Organic Light-Emitting Diode (OLED) display device has become a very important display technology in the field of flat panel display, due to its advantages such as small thickness, light weight, wide viewing angle, self-luminescence, capable of continuously adjusting wavelengths of light, low cost, rapid response, low power consumption, wide operation temperature range, simple production process, high luminescence efficiency, suitable for flexible display and the like.

[0003] Substances such as water vapor and oxygen in the air have a great influence on the service life of OLED in the OLED display device. Cathode of the OLED is typically made of metals such as aluminum, magnesium and calcium, and these metals are apt to react with the water vapor and oxygen permeated into the OLED display device because of their relatively active chemical nature. In addition, the water vapor and oxygen will also react with a hole transporting layer and an electron transporting layer of the OLED, and these reactions will cause failure of the OLED. Therefore, an effective encapsulation of the OLED display device to sufficiently separate respective functional layers of the OLED from the water vapor, oxygen and other substances in the atmosphere can greatly prolong the service life of the OLED and the service life of the OLED display device.

[0004] Encapsulation method of the OLED display device typically comprises two types: substrate encapsulation and thin film encapsulation (TFE). The substrate encapsulation refers to filling a glue film between a display substrate formed with the OLED and an encapsulation substrate, so that the glue film after being cured allows an airtight space to be formed between the display substrate and the encapsulation substrate. The thin film encapsulation refers to covering the surface of the OLED with a thin film encapsulation layer consisting of an inorganic thin film and an organic thin film, so that it is difficult for the water and oxygen to penetrate into the OLED.

[0005] FIG. 1 is a schematic cross-section view illustrating an OLED display device using thin film encapsulation. As shown in FIG. 1, the OLED display device comprises the thin film encapsulation layer covering the OLED (comprising a first electrode 110, a second electrode 113, and a light-emitting layer 112 provided therebetween), the thin film encapsulation layer comprises a plurality of first thin film encapsulation layers 130-1 and a plurality of second thin film encapsulation layers 130-2 which are alternately stacked. The first thin film encapsulation layer 130-1 is formed to be larger than the second thin film encapsulation layer 130-2 and envelops edges of the second thin film encapsulation layer 130-2, in order to limit prevent the water vapor and oxygen in the air entering into the OLED from the edges of the thin film encapsulation layer. In this case, it is necessary to respectively form the first thin film encapsulation layer 130-1 and the second thin film encapsulation layer 130-2 by using two different mask plates. Further, according to one technique, a plurality

of thin film encapsulation layers with gradually increasing sizes are sequentially formed on the OLED and an upper thin film encapsulation layer envelops edges of a lower thin film encapsulation layer to better limit or prevent the water vapor and oxygen in the air entering into the OLED from the edges of the thin film encapsulation layers. In this case, it is necessary to respectively form the plurality of thin film encapsulation layers by using a plurality of different mask plates.

[0006] As can be seen above, it is necessary to implement the thin film encapsulation by using at least two mask plates and to frequently replace the mask plates, which will increase production cost and render the production process complicated.

SUMMARY

[0007] According to embodiments of the disclosure, there is provided an encapsulation method of an OLED display device. The method comprises: providing a display substrate, the display substrate having a display region and a peripheral region provided outside the display region; forming a dam in the peripheral region of the display substrate; and forming a plurality of thin film encapsulation layers on the display substrate by using a single mask plate, wherein the plurality of thin film encapsulation layers envelop the dam therein.

[0008] For example, the display region of the display substrate has a plurality of pixel units arranged in a matrix; the display substrate comprises a pixel defining layer for defining the plurality of pixel units; and the method comprises: forming the dam on the pixel defining layer.

[0009] For example, the encapsulation method comprises: integrally forming the dam with the pixel defining layer.

[0010] For example, the display region of the display substrate has a plurality of pixel units arranged in a matrix; the display substrate comprises: a pixel defining layer for defining the plurality of pixel units; and a planarization layer provided below the pixel defining layer and planarizing the display substrate; and the method comprises: forming the dam on the planarization layer, and forming the pixel defining layer between the dam and the plurality of thin film encapsulation layers, wherein the pixel defining layer envelops the dam therein.

[0011] For example, the encapsulation method comprises: integrally forming the dam with the planarization layer.

[0012] For example, the display region of the display substrate has a plurality of pixel units arranged in a matrix, and each pixel unit comprises an OLED and a driving TFT for driving the OLED; the display substrate comprises: a pixel defining layer for defining the plurality of pixel units; a planarization layer provided below the pixel defining layer and planarizing the display substrate; and a passivation layer provided below the planarization layer and separating the driving TFT from the OLED; and the method comprises: forming the dam on the passivation layer, and sequentially forming the planarization layer and the pixel defining layer between the dam and the plurality of thin film encapsulation layers, wherein the planarization layer and the pixel defining layer envelop the dam therein.

[0013] For example, the encapsulation method comprises: integrally forming the dam with the passivation layer.

[0014] For example, the mask plate has an opening, and the opening is larger than or equal to a region surrounded by an outmost edge of the dam; upon placing the mask plate on the display substrate, the opening just exposes the whole dam; or upon placing the mask plate on the display substrate, the

opening exposes the whole dam and further exposes a portion of the surface of the display substrate which is provided outside the dam.

[0015] For example, a shape of a cross section of the dam taken along a direction perpendicular to the display substrate is a trapezoid with a width of an upper side less than a width of a lower side or a rectangular with the width of the upper side equal to the width of the lower side.

[0016] For example, the dam is formed as an endless closed ring so as to surround the display region.

[0017] According to embodiments of the disclosure, there is provided an OLED display device. The display device comprises: a display substrate, having a display region and a peripheral region provided outside the display region; a dam, formed in the peripheral region of the display substrate; and a plurality of encapsulation layers, formed on the display substrate and enveloping the dam therein.

[0018] For example, the display region of the display substrate has a plurality of pixel units arranged in a matrix; the display substrate comprises a pixel defining layer for defining the plurality of pixel units; and the dam is formed on the pixel defining layer.

[0019] For example, the dam is integrally formed with the pixel defining layer.

[0020] For example, the display region of the display substrate has a plurality of pixel units arranged in a matrix; the display substrate comprises: a pixel defining layer for defining the plurality of pixel units; and a planarization layer provided below the pixel defining layer and planarizing the display substrate; and the dam is formed on the planarization layer, the pixel defining layer is formed between the dam and the plurality of thin film encapsulation layers, and the pixel defining layer envelops the dam therein.

[0021] For example, the dam is integrally formed with the planarization layer.

[0022] For example, the display region of the display substrate has a plurality of pixel units arranged in a matrix, and each pixel unit comprises an OLED and a driving TFT for driving the OLED; the display substrate comprises: a pixel defining layer for defining the plurality of pixel units; a planarization layer provided below the pixel defining layer and planarizing the display substrate; and a passivation layer provided below the planarization layer and separating the driving TFT from the OLED; and the dam is formed on the passivation layer, the planarization layer and the pixel defining layer are sequentially formed between the dam and the plurality of thin film encapsulation layers, and the planarization layer and the pixel defining layer envelop the dam therein.

[0023] For example, the dam is integrally formed with the passivation layer.

[0024] For example, edges of the plurality of thin film encapsulation layers are provided on an outer wall of the dam, or the edges of the plurality of thin film encapsulation layers are provided on a portion of a surface of the display substrate which is provided outside the dam.

[0025] For example, a shape of a cross section of the dam taken along a direction perpendicular to the display substrate is a trapezoid with a width of an upper side less than a width of a lower side or a rectangular with the width of the upper side equal to the width of the lower side.

[0026] For example, the dam is formed as an endless closed ring so as to surround the display region.

BRIEF DESCRIPTION OF THE DRAWINGS

[0027] In order to clearly illustrate the technical solution of the embodiments of the disclosure, the drawings of the embodiments will be briefly described in the following; it is obvious that the described drawings are only related to some embodiments of the disclosure and thus are not limitative of the disclosure.

[0028] FIG. 1 is a cross-section view illustrating an OLED display device using thin film encapsulation;

[0029] FIG. 2 is a cross-section view illustrating an OLED display device according to embodiments of the disclosure;

[0030] FIG. 3 is a cross-section view illustrating the OLED display device according to the embodiments of the disclosure;

[0031] FIG. 4 is a cross-section view illustrating the OLED display device according to the embodiments of the disclosure;

[0032] FIG. 5 is a cross-section view illustrating the OLED display device according to the embodiments of the disclosure; and

[0033] FIG. 6 is a plan view illustrating the OLED display device according to the embodiments of the disclosure.

DETAILED DESCRIPTION

[0034] In order to make objects, technical details and advantages of the embodiments of the disclosure apparent, the technical solutions of the embodiment will be described in a clearly and fully understandable way in connection with the drawings related to the embodiments of the disclosure. It is obvious that the described embodiments are just a part but not all of the embodiments of the disclosure. Based on the described embodiments herein, those skilled in the art can obtain other embodiment(s), without any inventive work, which should be within the scope of the disclosure.

[0035] Unless otherwise defined, all the technical and scientific terms used herein have the same meanings as commonly understood by one of ordinary skill in the art to which the disclosure belongs. The terms "first," "second," etc., which are used in the description and the claims of the disclosure, are not intended to indicate any sequence, amount or importance, but distinguish various components. Also, the terms such as "a," "an," etc., are not intended to limit the amount, but indicate the existence of at least one. The terms "comprise," "comprising," "include," "including," etc., are intended to specify that the elements or the objects stated before these terms encompass the elements or the objects and equivalents thereof listed after these terms, but do not preclude the other elements or objects. "On," "under," "right," "left" and the like are only used to indicate relative position relationship, and when the position of the object which is described is changed, the relative position relationship may be changed accordingly.

[0036] The embodiments of the disclosure provide an OLED display device and an encapsulation method thereof, which can form a plurality of thin film encapsulation layers by using a single mask plate while delay permeation of water vapor and oxygen in the air from edges of the thin film encapsulation layers into an OLED of the OLED display device, thereby prolonging a service life of the OLED, simplifying a thin film encapsulation process and reducing cost of the thin film encapsulation process.

[0037] With reference now to FIG. 2 to FIG. 6, the OLED display device and the encapsulation method thereof according to the embodiments of the disclosure will be described in detail.

[0038] The encapsulation method of the OLED display device according to the embodiments of the disclosure comprises: providing a display substrate 1, the display substrate 1 having a display region 01 and a peripheral region 02 provided outside the display region 01; forming a dam 30 in the peripheral region 02 of the display substrate 1; and forming a plurality of thin film encapsulation layers 40 on the display substrate by using a single mask plate. The plurality of thin film encapsulation layers 40 envelop the dam 30 therein.

[0039] For example, the display region 01 of the display substrate 1 has a plurality of pixel units arranged in a matrix; each pixel unit comprises an OLED and a driving thin film transistor (briefly referred to as TFT) for driving the OLED.

[0040] With reference to FIG. 2 to FIG. 5, the display substrate 1 comprises a base substrate 10, and a buffer layer 11, an active layer 12, a gate insulating layer 13, a gate electrode layer, an interlayer insulating layer 15, a source-drain metal layer, a passivation layer 18 and a planarization layer 19 sequentially formed on the base substrate 10. The gate electrode layer comprises a gate electrode 14, and the source-drain metal layer comprises a source electrode 16 and a drain electrode 17. The source electrode 16 and the drain electrode 17 are connected to the active layer 12 through a via-hole passing through the interlayer insulating layer 15 and the gate insulating layer 13. The active layer 12, the gate insulating layer 13, the gate electrode 14, the interlayer insulating layer 15, the source electrode 16 and the drain electrode 17 constitute the driving TFT. The passivation layer 18 separates the driving TFT from the subsequently formed OLED. The planarization layer 19 planarizes a surface of the display substrate 1. It should be noted that, in addition to the driving TFT, the display substrate for example further comprises components such as a switching TFT, a storage capacitor, a gate line, a data line and the like. These components are the same as those in the art, which will not be described here for simplicity.

[0041] With further reference to FIG. 2 to FIG. 5, the display substrate 1 further comprises a pixel defining layer 21 and the OLED which are formed on the planarization layer 19. The pixel defining layer 21 defines the plurality of pixel units. The OLED is formed within each pixel unit and comprises a first electrode 20, a second electrode 23 and an organic light-emitting layer 22 disposed between the first electrode 20 and the second electrode 23. For example, the organic light-emitting layer comprises a hole injection layer, a hole transporting layer, a light-emitting layer, an electron transporting layer and an electron injection layer. The first electrode 20 of the OLED is connected to the drain electrode 17 of the driving TFT through a via-hole passing through the planarization layer 19 and the passivation layer 18.

[0042] It should be noted that, respective layers of the driving TFT, the passivation layer 18, the planarization layer 19, the pixel defining layer 21 and respective layers of the OLED may be made of any materials by any methods known in the art. The embodiments of the disclosure will not limit these materials and these methods.

[0043] It should be noted that, the driving TFT and the OLED may utilize any structures known in the art and the driving TFT and the OLED may have any relative position

relationships known in the art. The embodiments of the disclosure will not limit these structures and these relative position relationships.

[0044] After the display substrate 1 is provided, the dam 30 is formed in the peripheral region 02 of the display substrate 1. For example, the dam 30 is formed by a patterning process using a single-tone mask plate. For example, the dam has a thickness of about 2~5 μm , and a width of about 50~500 μm .

[0045] For example, the dam is made of an inorganic material, and the inorganic material is Al_2O_3 , TiO_2 , ZrO_2 , MgO , HfO_2 , Ta_2O_5 , Si_3N_4 , AlN , SiN , SiNO , SiO , SiO_2 , SiO_x , SiC , ITO or combinations thereof.

[0046] For example, the dam is made of an organic material, and the organic material is PET (polyethylene terephthalate), PEN (polyethylene naphthalate), PC (polycarbonate), PI (polyimide), PVC (polyvinyl chloride), PS (polystyrene), PMMA (polymethyl methacrylate), PBT (polybutylene terephthalate), PSO (polysulfone), PES (polyparaphenylene diethyl sulfone), PE (polyethylene), PP (polypropylene), polysiloxane, PA (polyamide), PVDF (polyvinylidene fluoride), EVA (ethylene-vinyl acetate copolymer), EVAL (ethylene-vinyl alcohol copolymer), PAN (polyacrylonitrile), PVAc (polyvinyl acetate), Parylene, Polyurea, PTFE (polytetrafluoroethylene), epoxy resin or combinations thereof.

[0047] For example, the dam is made of a combination of one or more of the above-mentioned inorganic materials and one or more of the above-mentioned organic materials.

[0048] For example, as to a cross section of the dam 30 taken along a direction perpendicular to the display substrate 1, a width of an upper side of the cross section is less than or equal to a width of a lower side of the cross section, in order that the plurality of thin film encapsulation layers 40 formed subsequently closely cover the OLED and there is no gap between the thin film encapsulation layers 40 and the OLED. Further, for example, a shape of the cross section is a trapezoid with the width of the upper side less than the width of the lower side or a rectangular with the width of the upper side equal to the width of the lower side.

[0049] It should be noted that the "upper side" indicates a side of the cross section away from the display substrate 1, and the "lower side" indicates a side of the cross section close to the display substrate 1.

[0050] Further, in order that the plurality of thin film encapsulation layers 40 formed subsequently are closely contact the dam 30, for example, the shape of the cross section of the dam is a trapezoid without right angle, that is, right and left sides of the cross section form acute angles with the display substrate 1.

[0051] For example, the dam 30 is formed as an endless closed ring so as to surround the display region 01, as shown in FIG. 6. However, the embodiments of the disclosure are not limited thereto, and the dam 30 may be provided as a ring having an opening as practically required.

[0052] With further reference to FIG. 2 to FIG. 5, after formation of the dam 30, the plurality of thin film encapsulation layers 40 are formed by using the single mask plate (not shown), and the plurality of thin film encapsulation layers 40 cover the OLED and envelop the dam 30 therein. For example, the mask plate has an opening, the mask plate is placed on the display substrate 1 after formation of the dam 30, and a plurality of thin films for forming the thin film encapsulation layers are sequentially formed for example by plasma enhanced chemical vapor deposition (briefly referred to as PECVD). After formation of the thin films for forming

the thin film encapsulation layers, the mask plate and portions of the thin films for forming the thin film encapsulation layers provided on the non-opening region of the mask plate are removed to obtain the plurality of thin film encapsulation layers 40, and the plurality of thin film encapsulation layers 40 cover the OLED and envelop the dam 30 therein.

[0053] For example, the opening of the mask plate is larger than or equal to a region surrounded by an outmost edge of the dam 30, that is, the opening of the mask plate at least covers the display region of the display substrate and a region from the outmost edge of the dam 30 to the display region. In the case that the opening of the mask plate is equal to the region surrounded by the outmost edge of the dam 30, after the mask plate is placed on the display substrate 1, the opening of the mask plate just exposes the whole dam 30 but does not expose the surface of the display substrate 1 which is provided outside the dam 30. In this case, edges of the plurality of thin film encapsulation layers 40 are provided on an outer wall of the dam 30 as shown in FIG. 2 to FIG. 4, which greatly increases a distance between the edges of the plurality of thin film encapsulation layers 40 and the OLED, so that a distance that the water vapor and oxygen enters from the edges of the plurality of thin film encapsulation layers 40 to the OLED are increased and permeation of water vapor and oxygen from the edges of the plurality of thin film encapsulation layers 40 into the OLED are delayed.

[0054] In the case that the opening of the mask plate is larger than the region surrounded by the outmost edge of the dam 30, after the mask plate is placed on the display substrate 1, the opening exposes the whole dam 30 and further exposes a portion of the surface of the display substrate 1 which is provided outside the dam 30. In this case, the edges of the plurality of thin film encapsulation layers 40 are provided on the surface of the display substrate 1 outside the dam 30 as shown in FIG. 5, which further increases the distance between the edges of the plurality of thin film encapsulation layers 40 and the OLED, so that the distance that the water vapor and oxygen enters from the edges of the plurality of thin film encapsulation layers 40 to the OLED are further increased and permeation of the water vapor and oxygen from the edges of the plurality of thin film encapsulation layers 40 into the OLED are further delayed. It should be noted that the outmost edge or the outside of the dam 30 indicates an edge or a side away from the display region.

[0055] For example, the region surrounded by the outmost edge of the dam 30 is a region surrounded by an intersecting line where the outer wall of the dam 30 and the display substrate 1 intersect.

[0056] For example, the plurality of thin film encapsulation layers 40 comprise a plurality of inorganic thin film encapsulation layers 41 and a plurality of organic thin film encapsulation layers 42 alternately stacked. Here, the specific number of the plurality of thin film encapsulation layers 40 is not limited. For example, taking the ability of resisting oxygen and water and the lighter and thinner appearance into account, 2-20 thin film encapsulation layers 40 are formed. For example, a thickness of the inorganic thin film encapsulation layers 41 is 5-20 nm, and a thickness of the organic thin film encapsulation layers 42 is 5 nm-20 nm. It should be noted that the plurality of thin film encapsulation layers 40 are made of any known materials by any known methods, which will not be limited in the embodiments of the disclosure.

[0057] With reference to FIG. 2, the dam 30 is formed on the pixel defining layer 21. In this case, the dam 30 for

example is integrally formed with the pixel defining layer 21 in order to simplify the manufacture process. It should be noted that, that “the dam 30 is integrally formed with the pixel defining layer 21” indicates that the dam 30 and the pixel defining layer 21 are formed simultaneously by the same material and the same patterning process.

[0058] With reference to FIG. 3, the dam 30 is formed on the planarization layer 19, the pixel defining layer 21 envelops the dam 30 therein, and the plurality of thin film encapsulation layers 40 envelop the pixel defining layer 21 therein, which further increases the distance between the edges of the plurality of thin film encapsulation layers 40 and the OLED. In this case, the dam 30 for example is integrally formed with the planarization layer 19 in order to simplify the manufacture process. Likewise, that “the dam 30 is integrally formed with the planarization layer 19” indicates that the dam 30 and the planarization layer 19 are formed simultaneously by the same material and the same patterning process.

[0059] With reference to FIG. 4, the dam 30 is formed on the passivation layer 18, the planarization layer 19 and the pixel defining layer 21 sequentially envelop the dam 30 therein, and the plurality of thin film encapsulation layers 40 envelop the pixel defining layer 21 therein, which further increases the distance between the edges of the plurality of thin film encapsulation layers 40 and the OLED. In this case, the dam 30 for example is integrally formed with the passivation layer 18 in order to simplify the manufacture process. Likewise, that “the dam 30 is integrally formed with the passivation layer 18” indicates that the dam 30 and the passivation layer 18 are formed simultaneously by the same material and the same patterning process.

[0060] As shown in FIG. 5, the edges of the plurality of thin film encapsulation layers 40 are provided on the surface of the display substrate 1 outside the dam 30. In this case, although the dam 30 is formed on the pixel defining layer 21 as shown in FIG. 5, the dam 30 may be formed on the planarization layer 19 or the passivation layer 18 similarly as in FIG. 3 or FIG. 4.

[0061] In the encapsulation method of the OLED display device according to the embodiments of the disclosure, the dam is formed in the peripheral region of the display substrate 1, the plurality of thin film encapsulation layers are formed on the display substrate by using the single mask plate, and the plurality of thin film encapsulation layers envelop the dam therein. Since the dam is formed in the peripheral region of the display substrate, the plurality of thin film encapsulation layers can be formed on the display substrate by using the single mask plate, which thereby simplifies the thin film encapsulation process and reduces cost of the film encapsulation process. Further, since the dam is formed in the peripheral region of the display substrate, the edges of the plurality of thin film encapsulation layers are provided on the outer wall of the dam or provided on the surface of the display substrate outside the dam, which increases the distance between the edges of the plurality of thin film encapsulation layers and the OLED, so that the distance that the water vapor and oxygen enters from the edges of the plurality of thin film encapsulation layers to the OLED is increased, permeation of the water vapor and oxygen from the edges of the plurality of thin film encapsulation layers into the OLED is delayed and thus the service life of the OLED and the OLED display device is prolonged.

[0062] According to embodiments of the disclosure, there is provided an OLED display device. The OLED display

device comprises: a display substrate **1**, the display substrate **1** having a display region **01** and a peripheral region **02** provided outside the display region **01**; a dam **30**, formed in the peripheral region **02** of the display substrate **1**; and a plurality of thin film encapsulation layers **40**, formed on the display substrate **1** and enveloping the dam **30** therein. In the OLED display device, edges of the plurality of thin film encapsulation layers **40** are provided on the outer wall of the dam **30**, or the edges of the plurality of thin film encapsulation layers **40** are provided on the surface of the display substrate **1** provided outside the dam **30**. The above descriptions may be referred to for specific structures and technical effects of the OLED display device, which will not be repeated here.

[0063] The foregoing embodiments merely are exemplary embodiments of the disclosure, and not intended to define the scope of the disclosure, and the scope of the disclosure is determined by the appended claims.

[0064] The disclosure claims priority to and contains subject matters related to that disclosed in Chinese Priority Patent Application CN201410381365.3 filed on Aug. 5, 2014, the entire content of which is hereby incorporated by reference.

What is claimed is:

1. An encapsulation method of an OLED display device, comprising:

providing a display substrate, the display substrate having a display region and a peripheral region provided outside the display region;

forming a dam in the peripheral region of the display substrate; and

forming a plurality of thin film encapsulation layers on the display substrate by using a single mask plate, wherein the plurality of thin film encapsulation layers envelop the dam therein.

2. The encapsulation method according to claim 1, wherein the display region of the display substrate has a plurality of pixel units arranged in a matrix;

the display substrate comprises a pixel defining layer for defining the plurality of pixel units; and
the method comprises: forming the dam on the pixel defining layer.

3. The encapsulation method according to claim 2, comprising:

integrally forming the dam with the pixel defining layer.

4. The encapsulation method according to claim 1, wherein the display region of the display substrate has a plurality of pixel units arranged in a matrix;

the display substrate comprises: a pixel defining layer for defining the plurality of pixel units; and a planarization layer provided below the pixel defining layer and planarizing the display substrate; and

the method comprises: forming the dam on the planarization layer, and forming the pixel defining layer between the dam and the plurality of thin film encapsulation layers, wherein the pixel defining layer envelops the dam therein.

5. The encapsulation method according to claim 4, comprising:

integrally forming the dam with the planarization layer.

6. The encapsulation method according to claim 1, wherein the display region of the display substrate has a plurality of pixel units arranged in a matrix, and each pixel unit comprises an OLED and a driving TFT for driving the OLED;

the display substrate comprises: a pixel defining layer for defining the plurality of pixel units; a planarization layer provided below the pixel defining layer and planarizing the display substrate; and a passivation layer provided below the planarization layer and separating the driving TFT from the OLED; and

the method comprises: forming the dam on the passivation layer, and sequentially forming the planarization layer and the pixel defining layer between the dam and the plurality of thin film encapsulation layers, wherein the planarization layer and the pixel defining layer envelop the dam therein.

7. The encapsulation method according to claim 6, comprising:

integrally forming the dam with the passivation layer.

8. The encapsulation method according to claim 1, wherein the mask plate has an opening, and the opening is larger than or equal to a region surrounded by an outmost edge of the dam;

upon placing the mask plate on the display substrate, the opening just exposes the whole dam; or upon placing the mask plate on the display substrate, the opening exposes the whole dam and further exposes a portion of the surface of the display substrate which is provided outside the dam.

9. The encapsulation method according to claim 1, wherein a shape of a cross section of the dam taken along a direction perpendicular to the display substrate is a trapezoid with a width of an upper side less than a width of a lower side or a rectangular with the width of the upper side equal to the width of the lower side.

10. The encapsulation method according to claim 1, wherein

the dam is formed as an endless closed ring so as to surround the display region.

11. An OLED display device, comprising:

a display substrate, having a display region and a peripheral region provided outside the display region;

a dam, formed in the peripheral region of the display substrate; and

a plurality of encapsulation layers, formed on the display substrate and enveloping the dam therein.

12. The display device according to claim 11, wherein the display region of the display substrate has a plurality of pixel units arranged in a matrix;

the display substrate comprises a pixel defining layer for defining the plurality of pixel units; and
the dam is formed on the pixel defining layer.

13. The display device according to claim 12, wherein the dam is integrally formed with the pixel defining layer.

14. The display device according to claim 11, wherein the display region of the display substrate has a plurality of pixel units arranged in a matrix;

the display substrate comprises: a pixel defining layer for defining the plurality of pixel units; and a planarization layer provided below the pixel defining layer and planarizing the display substrate; and

the dam is formed on the planarization layer, the pixel defining layer is formed between the dam and the plurality of thin film encapsulation layers, and the pixel defining layer envelops the dam therein.

15. The display device according to claim 14, wherein the dam is integrally formed with the planarization layer.

16. The display device according to claim 11, wherein the display region of the display substrate has a plurality of pixel units arranged in a matrix, and each pixel unit comprises an OLED and a driving TFT for driving the OLED;

the display substrate comprises: a pixel defining layer for defining the plurality of pixel units; a planarization layer provided below the pixel defining layer and planarizing the display substrate; and a passivation layer provided below the planarization layer and separating the driving TFT from the OLED; and

the dam is formed on the passivation layer, the planarization layer and the pixel defining layer are sequentially formed between the dam and the plurality of thin film encapsulation layers, and the planarization layer and the pixel defining layer envelop the dam therein.

17. The display device according to claim 16, wherein the dam is integrally formed with the passivation layer.

18. The display device according to claim 11, wherein edges of the plurality of thin film encapsulation layers are provided on an outer wall of the dam, or the edges of the plurality of thin film encapsulation layers are provided on a portion of a surface of the display substrate which is provided outside the dam.

19. The display device according to claim 11, wherein a shape of a cross section of the dam taken along a direction perpendicular to the display substrate is a trapezoid with a width of an upper side less than a width of a lower side or a rectangular with the width of the upper side equal to the width of the lower side.

20. The display device according to claim 11, wherein, the dam is formed as an endless closed ring so as to surround the display region.

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摘要(译)

本发明的实施例提供一种有机发光二极管 (OLED) 显示装置及其封装方法。 OLED显示装置的封装方法包括：提供显示基板，显示基板具有显示区域和设置在显示区域外部的的外围区域;在显示基板的周边区域形成挡板;通过使用单个掩模板在显示基板上形成多个薄膜封装层，其中多个薄膜封装层将坝包围在其中。

